

Fig. 1

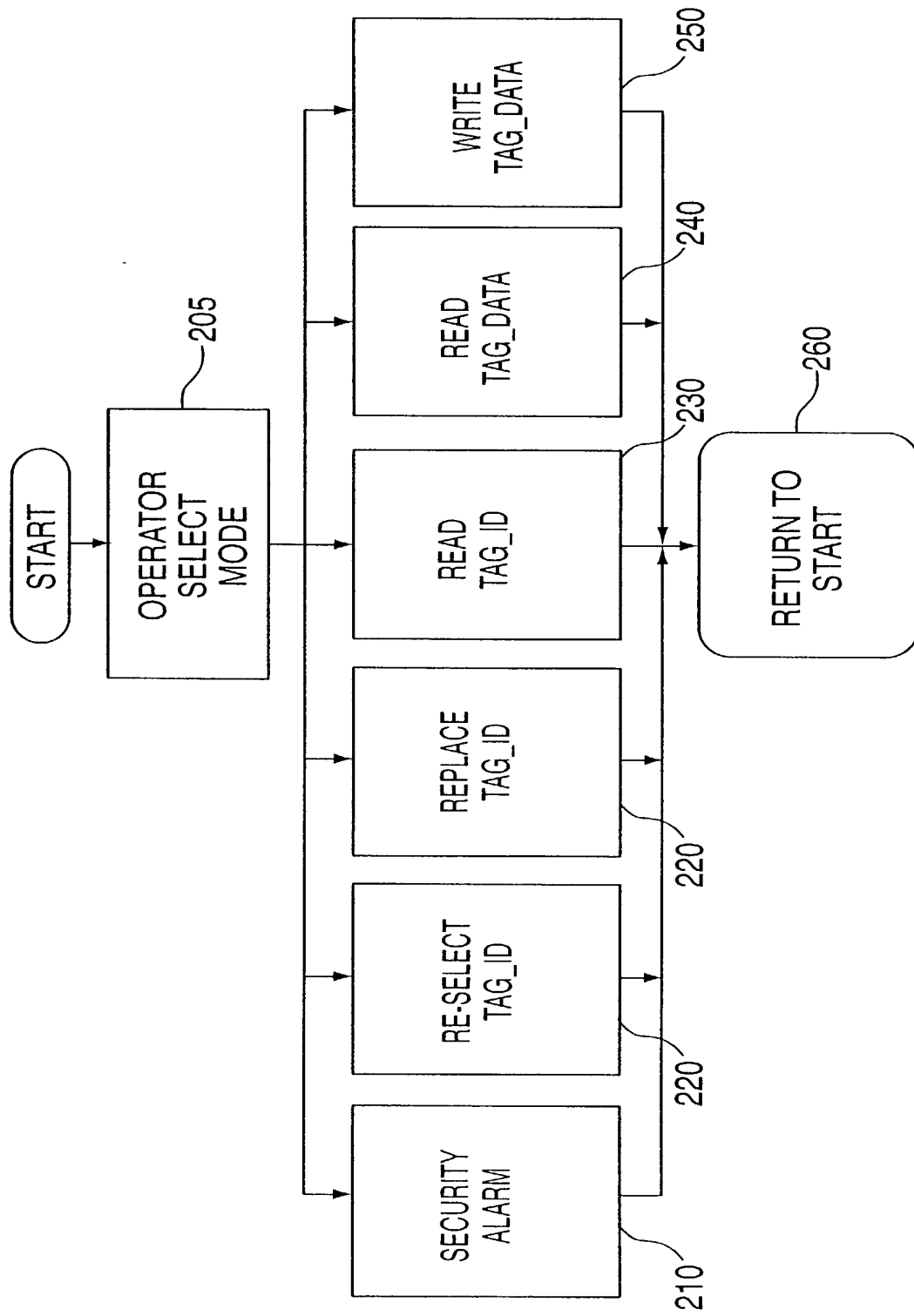


FIG. 2

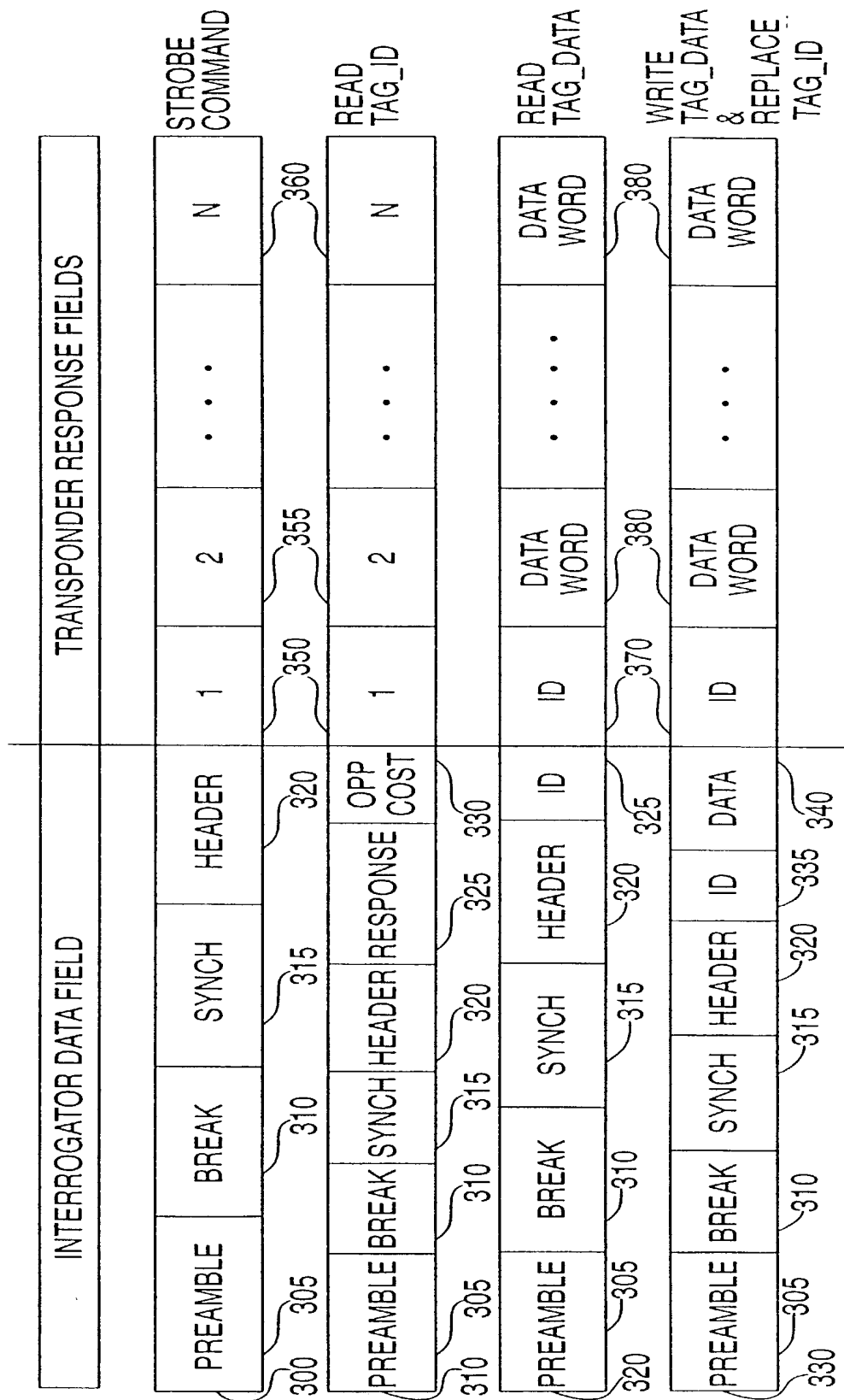


FIG. 3

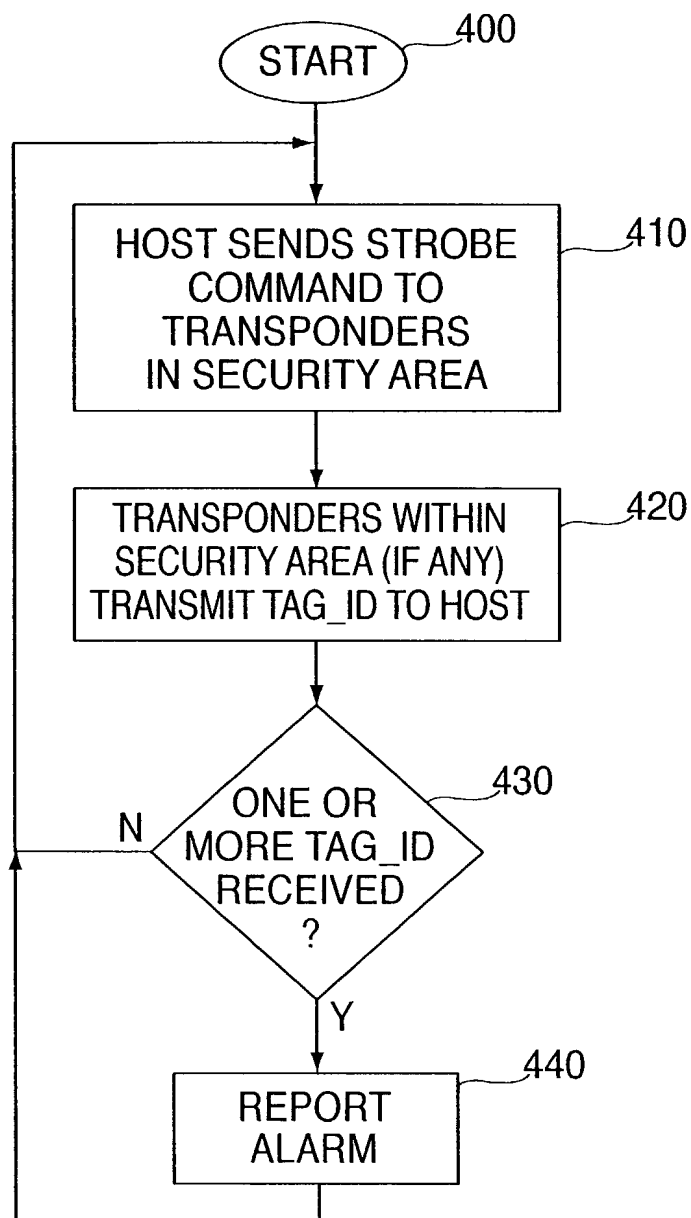


FIG. 4

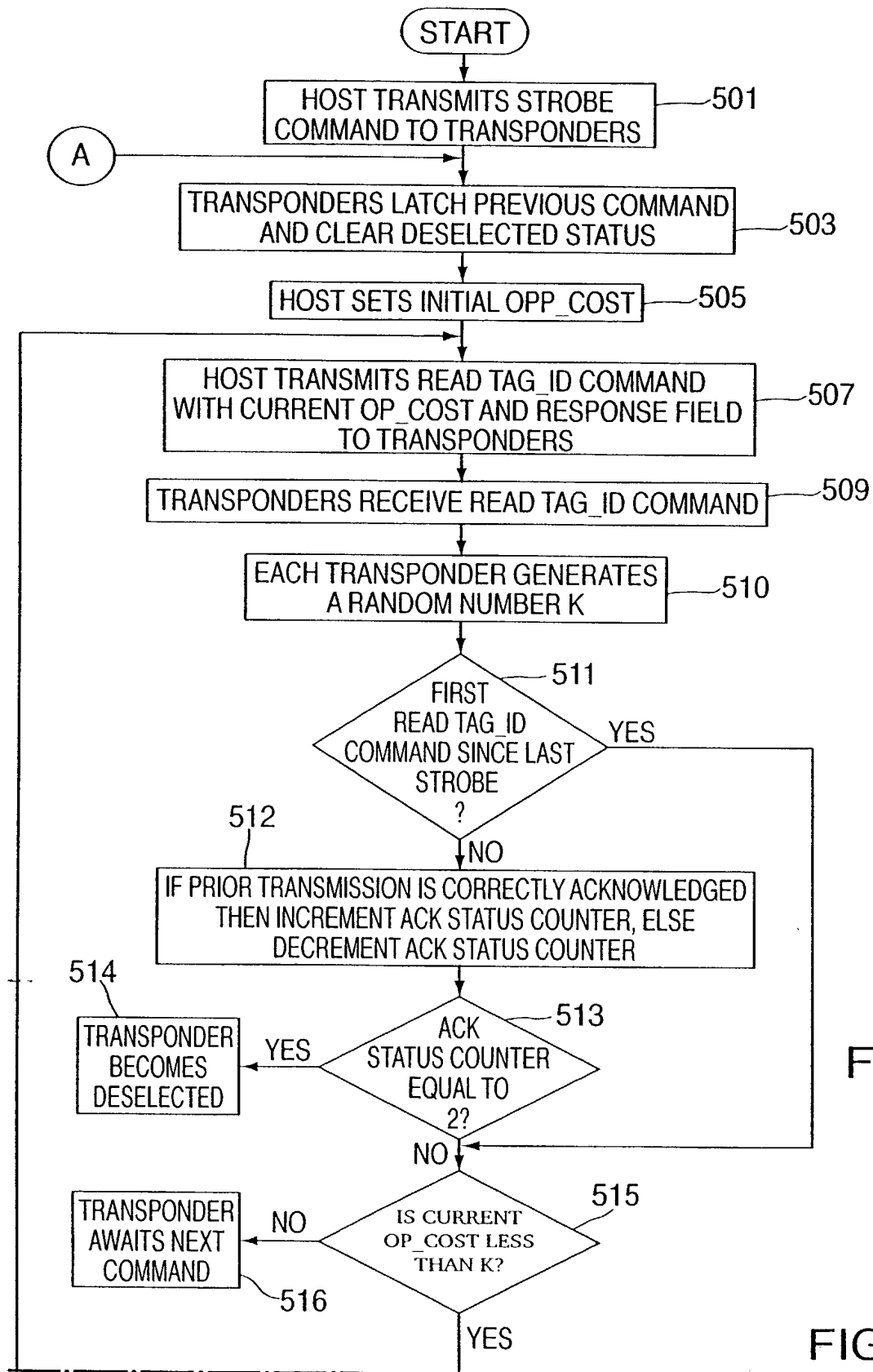


FIG. 5A

5A  
5B  
FIG. 5

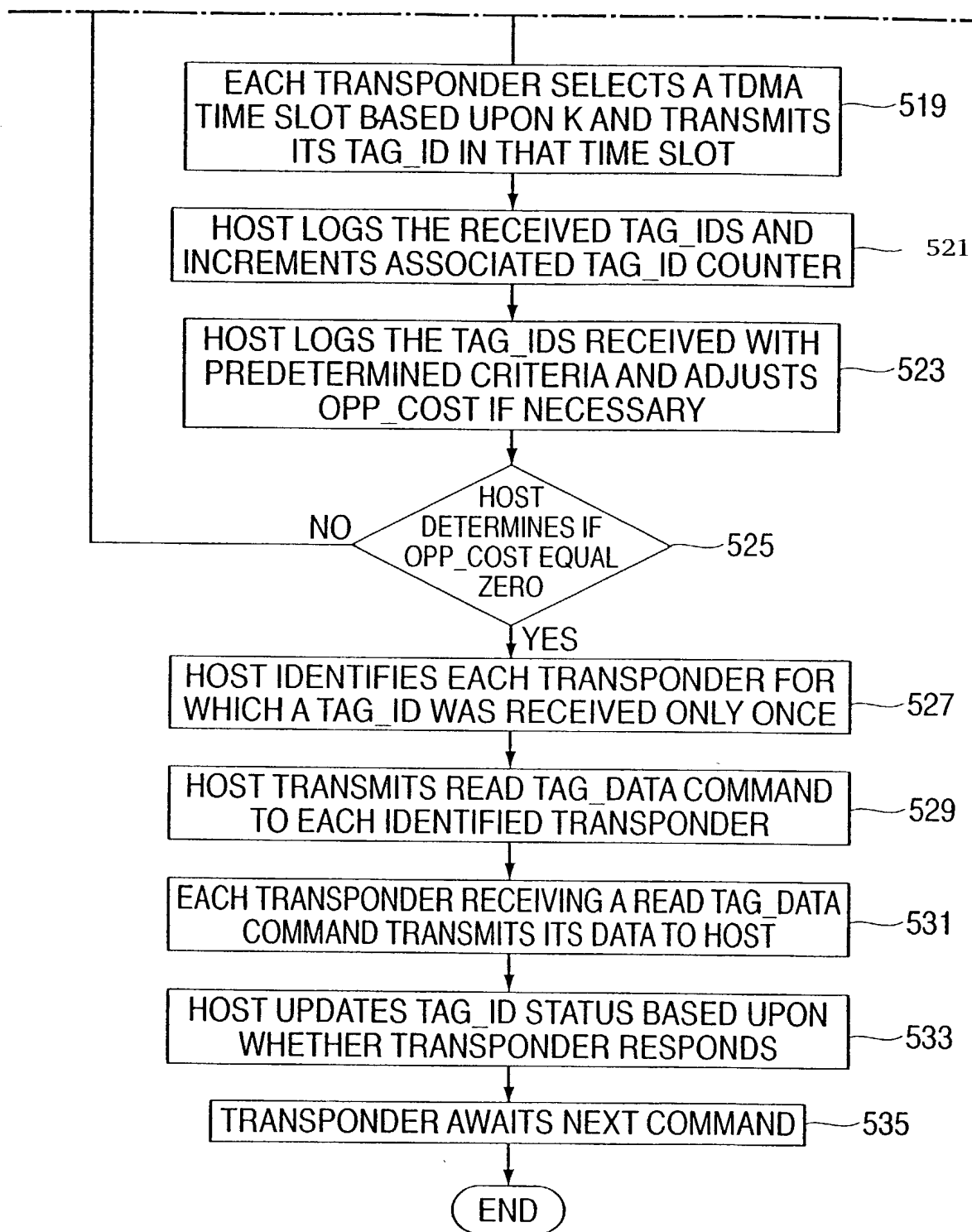


FIG. 5B

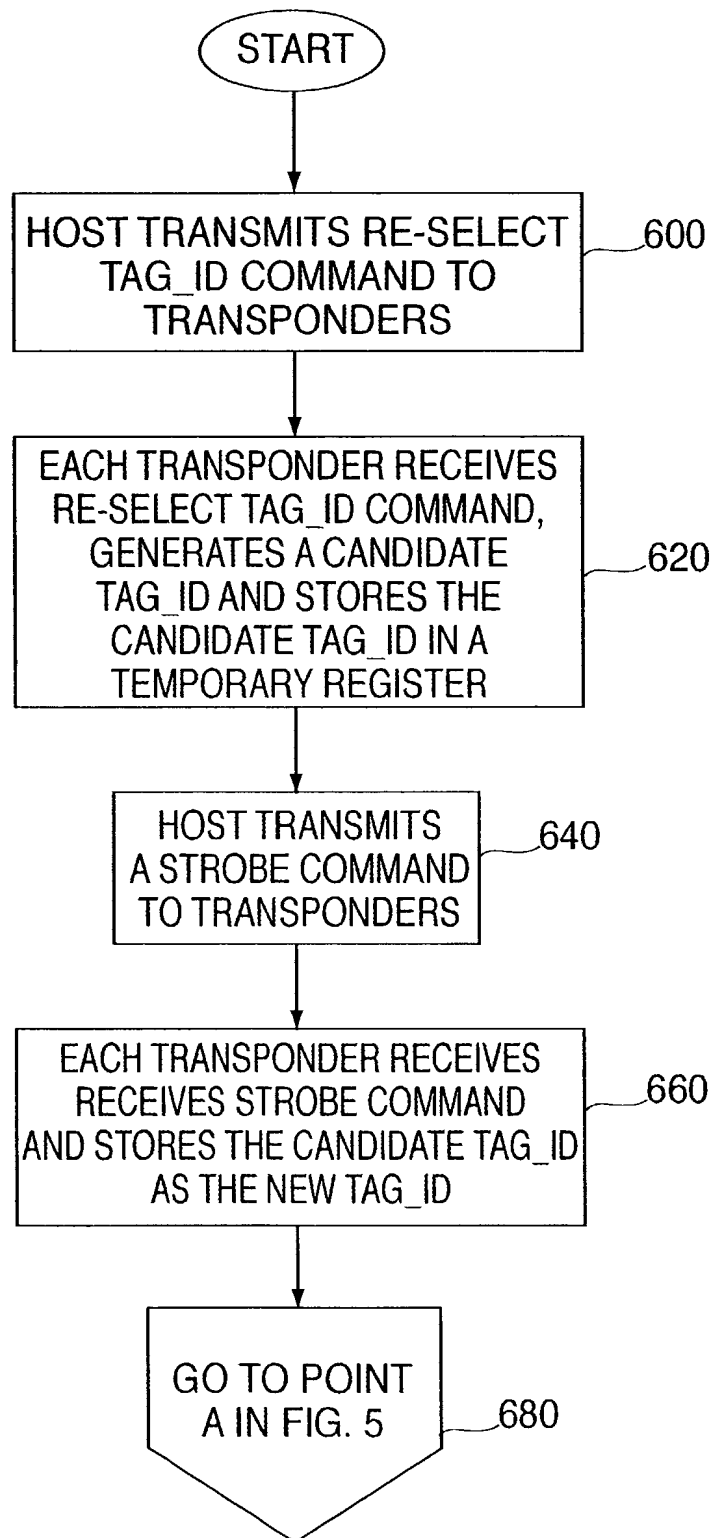


FIG. 6

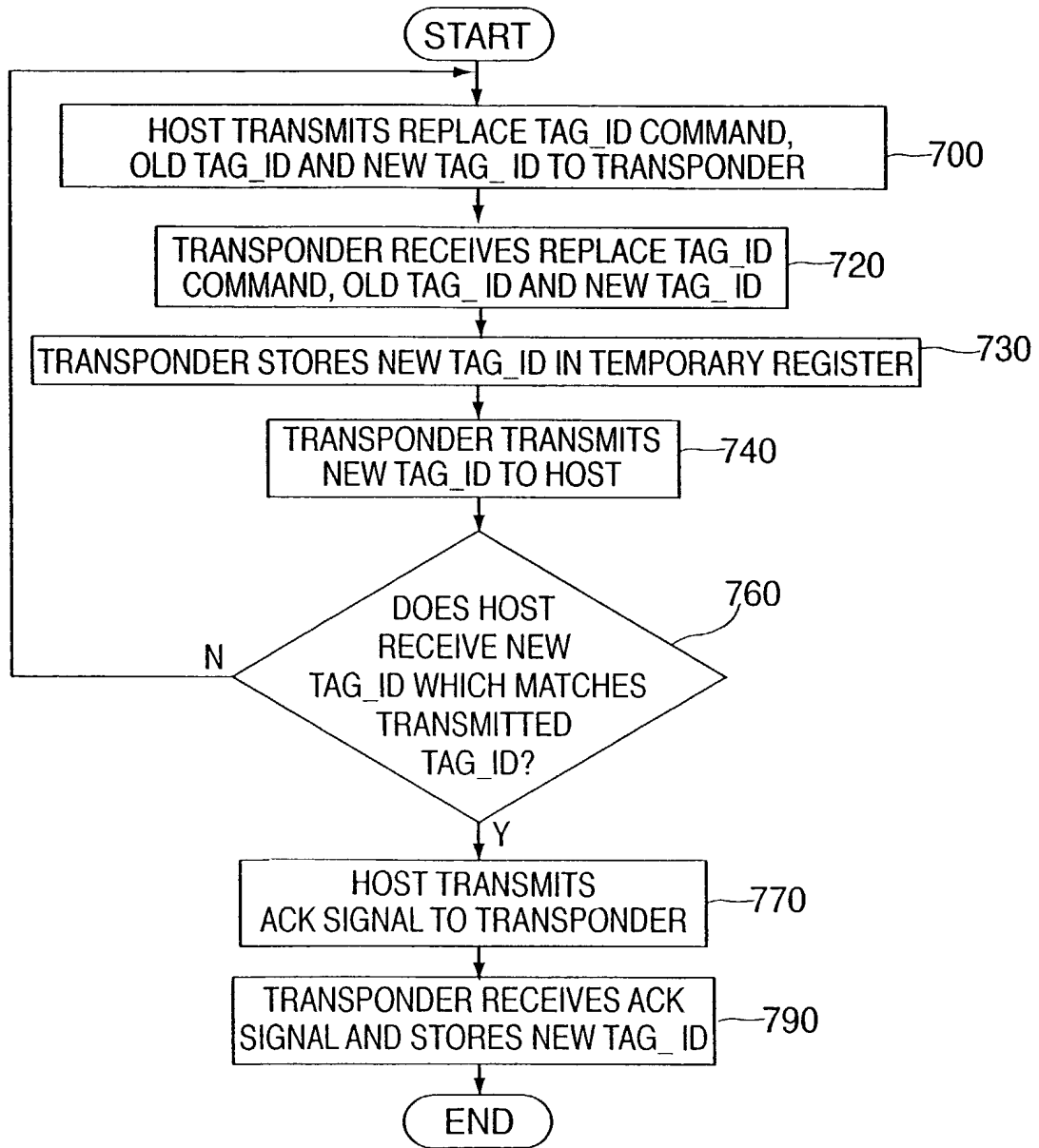


FIG. 7



```
graph TD
    START([START]) --> 800[HOST TRANSMITS READ TAG_DATA COMMAND INCLUDING SPECIFIC TAG_ID]
    800 --> 820[TRANSPONDERS RECEIVE READ TAG_DATA COMMAND AND TAG_ID]
    820 --> 830{EACH TRANSPONDER COMPARES RECEIVED TAG_ID TO ITS TAG_ID}
    830 -- N --> 820
    830 -- Y --> 840[SELECTED TRANSPONDER TRANSMITS TAG_ID AND TAG_DATA TO HOST]
    840 --> 870{HOST COMPUTER DETERMINES IF PORTION OF TAG_DATA RECEIVED INTACT}
    870 -- N --> 820
    870 -- Y --> 880[HOST COMPUTER STORES INTACT TAG_DATA]
    880 --> 890{HOST COMPUTER DETERMINES IF ALL TAG_DATA RECEIVED INTACT?}
    890 -- N --> 820
    890 -- Y --> END([END])
```

FIG. 8 is a flowchart of the host computer reading a tag. The process begins with a START terminal, leading to step 800: HOST TRANSMITS READ TAG\_DATA COMMAND INCLUDING SPECIFIC TAG\_ID. This leads to step 820: TRANSPONDERS RECEIVE READ TAG\_DATA COMMAND AND TAG\_ID. From 820, the flow goes to decision 830: EACH TRANSPONDER COMPARES RECEIVED TAG\_ID TO ITS TAG\_ID. If the answer is N (No), the flow loops back to 820. If the answer is Y (Yes), the flow goes to step 840: SELECTED TRANSPONDER TRANSMITS TAG\_ID AND TAG\_DATA TO HOST. From 840, the flow goes to decision 870: HOST COMPUTER DETERMINES IF PORTION OF TAG\_DATA RECEIVED INTACT. If the answer is N (No), the flow loops back to 820. If the answer is Y (Yes), the flow goes to step 880: HOST COMPUTER STORES INTACT TAG\_DATA. From 880, the flow goes to decision 890: HOST COMPUTER DETERMINES IF ALL TAG\_DATA RECEIVED INTACT?. If the answer is N (No), the flow loops back to 820. If the answer is Y (Yes), the flow goes to an END terminal.

FIG. 8

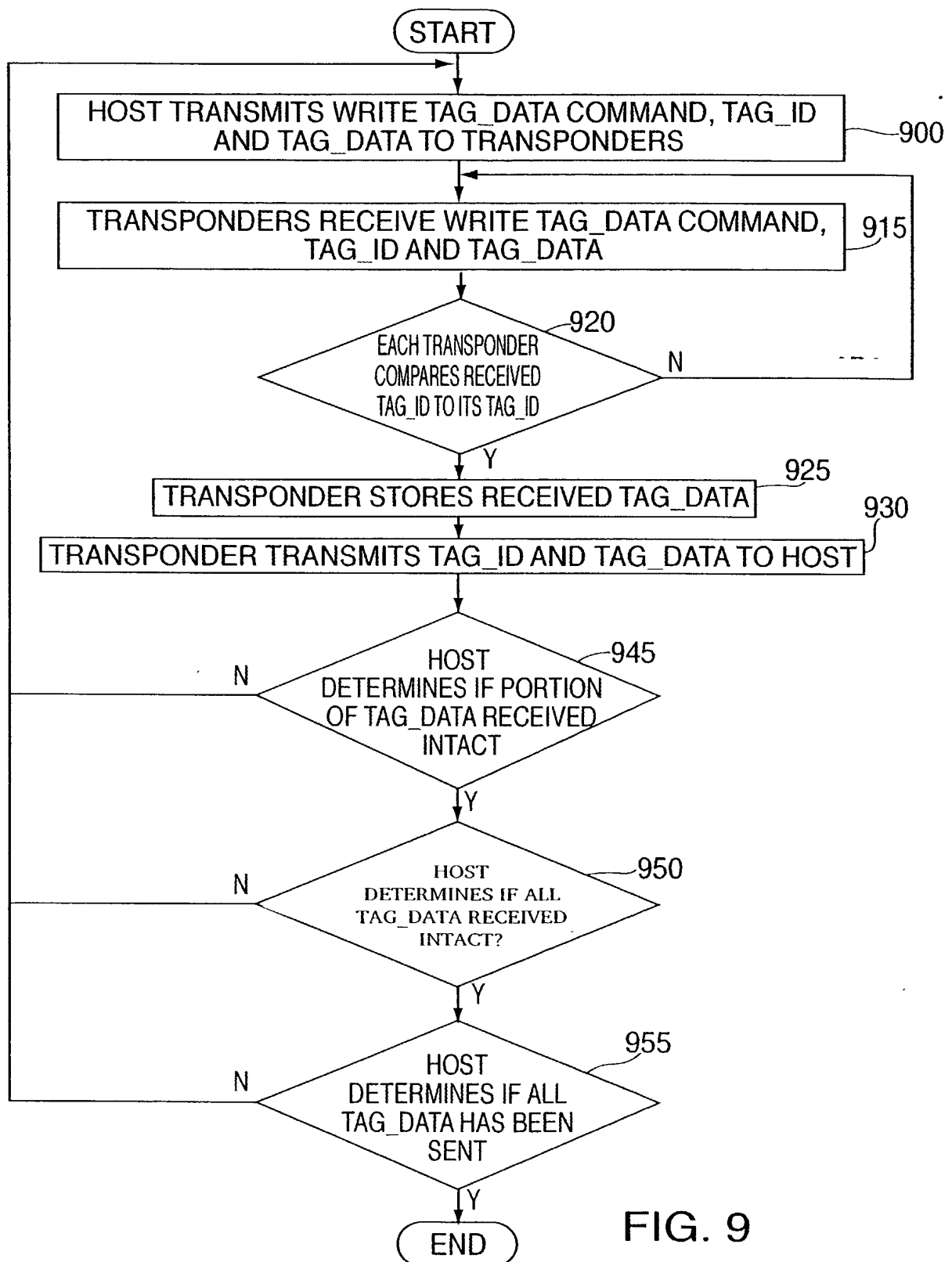


FIG. 9

FIG. 10A is a block diagram of a system 1000. The system 1000 includes a processor/controller 1000, a spread spectrum transmitter 1001, a delay 1017, a spread spectrum receiver 1002, a switch matrix 1003, and a feedback loop 1007. The processor/controller 1000 is connected to the spread spectrum transmitter 1001 and the spread spectrum receiver 1002. The spread spectrum transmitter 1001 is connected to the delay 1017, which is connected to the spread spectrum receiver 1002. The spread spectrum receiver 1002 is connected to the switch matrix 1003. The switch matrix 1003 is connected to the processor/controller 1000 and the feedback loop 1007. The feedback loop 1007 is connected to the spread spectrum receiver 1002. The system 1000 also includes three input ports 1004, 1005, and 1006, which are connected to the switch matrix 1003. The system 1000 is labeled "FROM/TO HOST" on the right side.

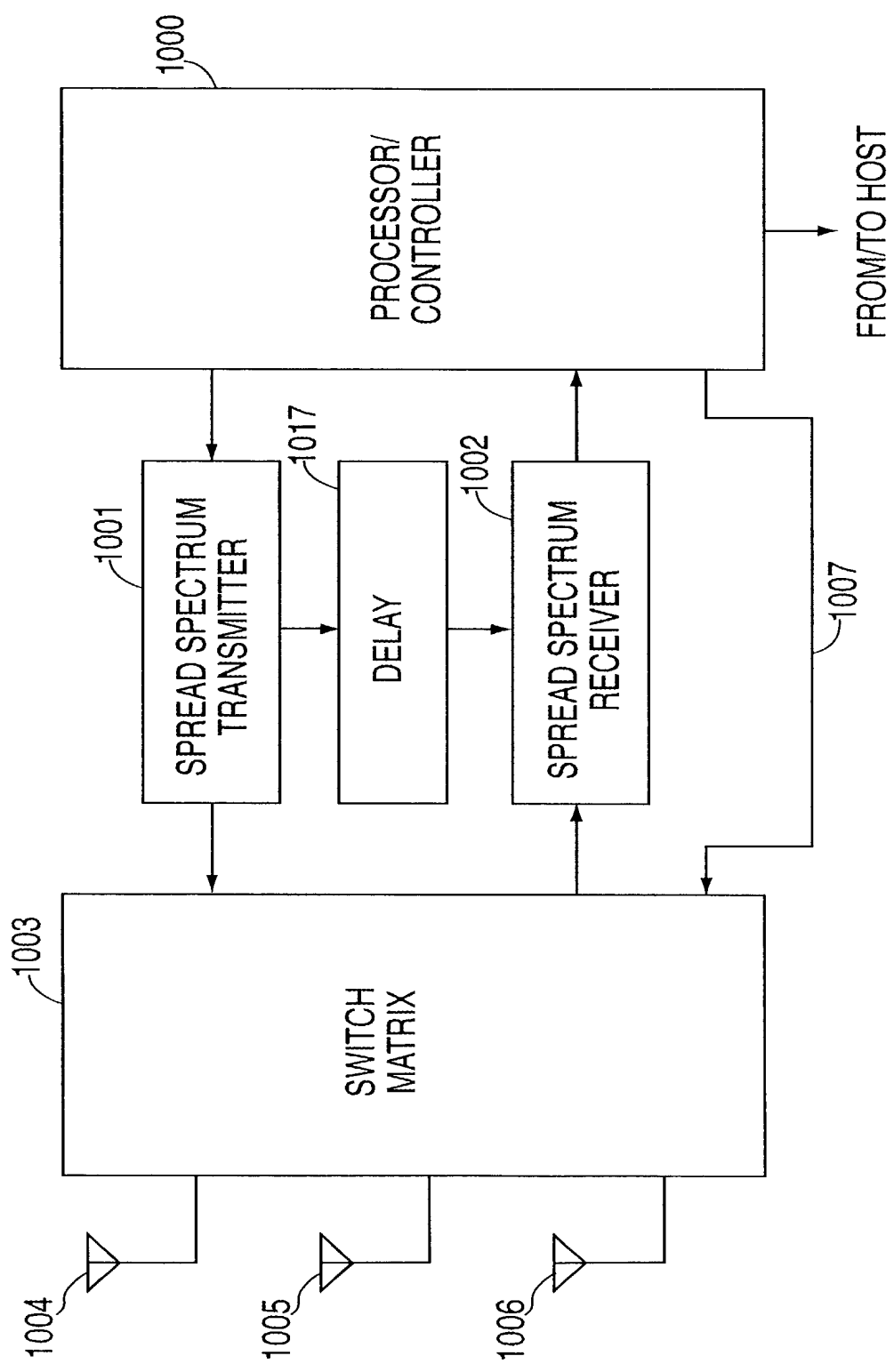


FIG. 10A

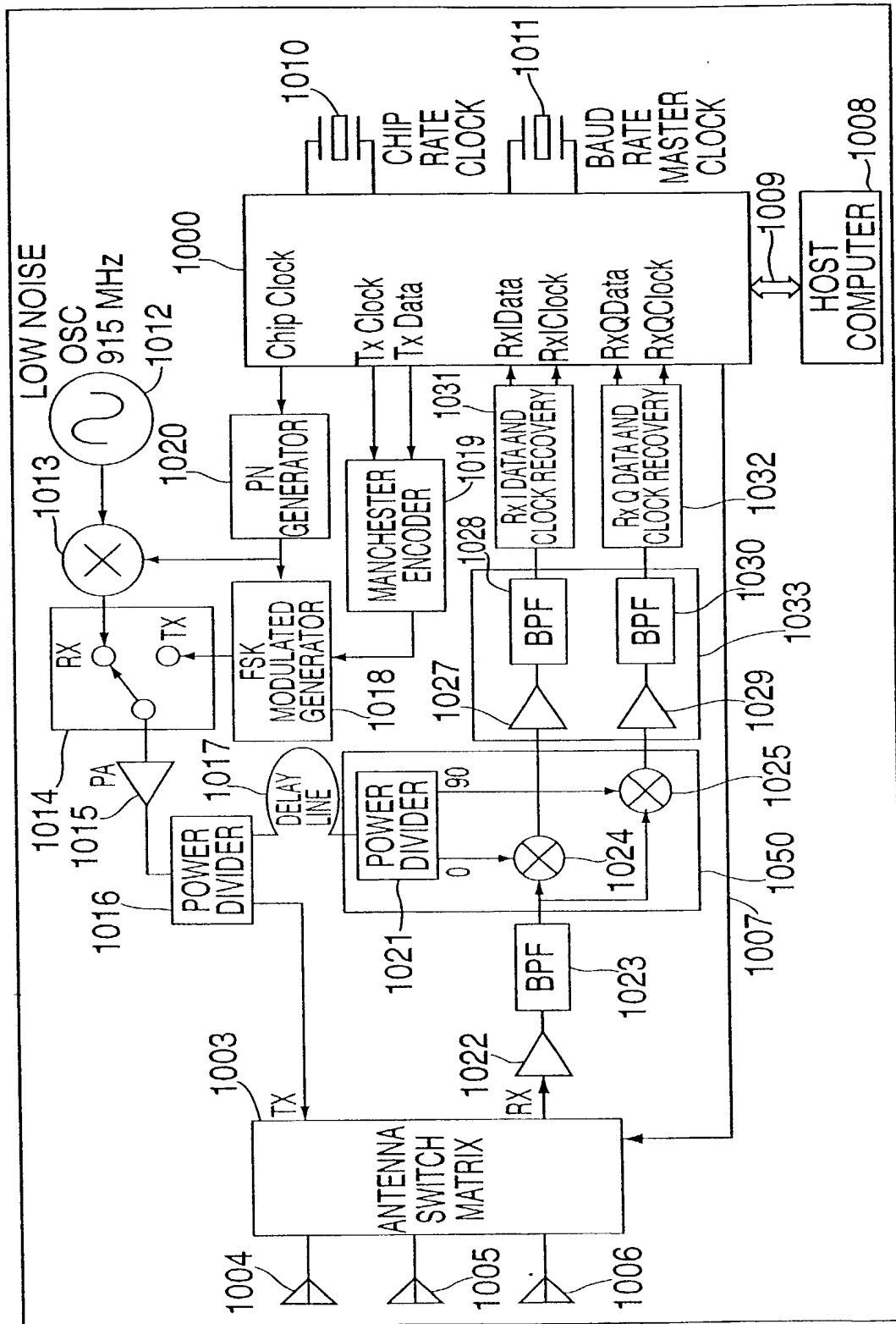


FIG. 10B

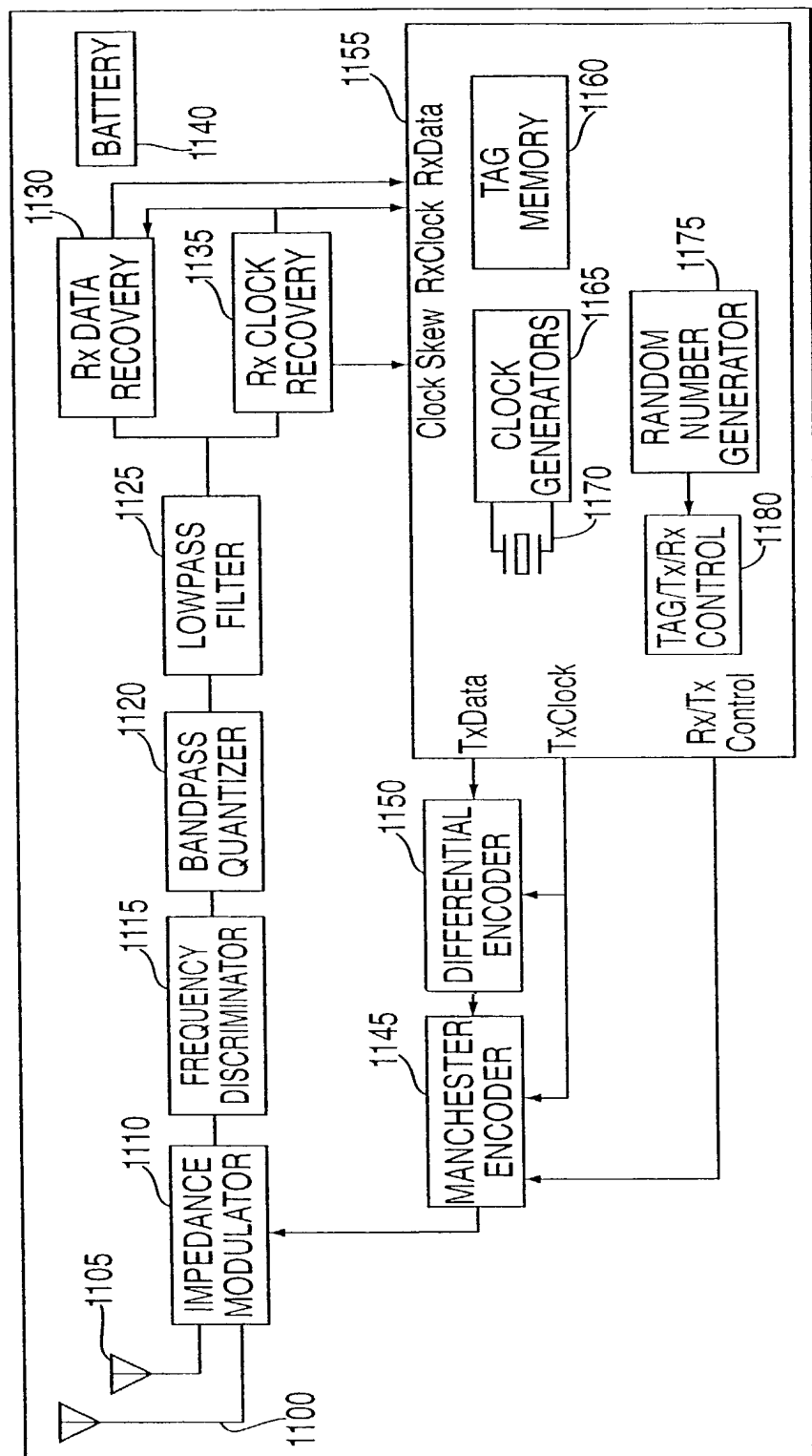
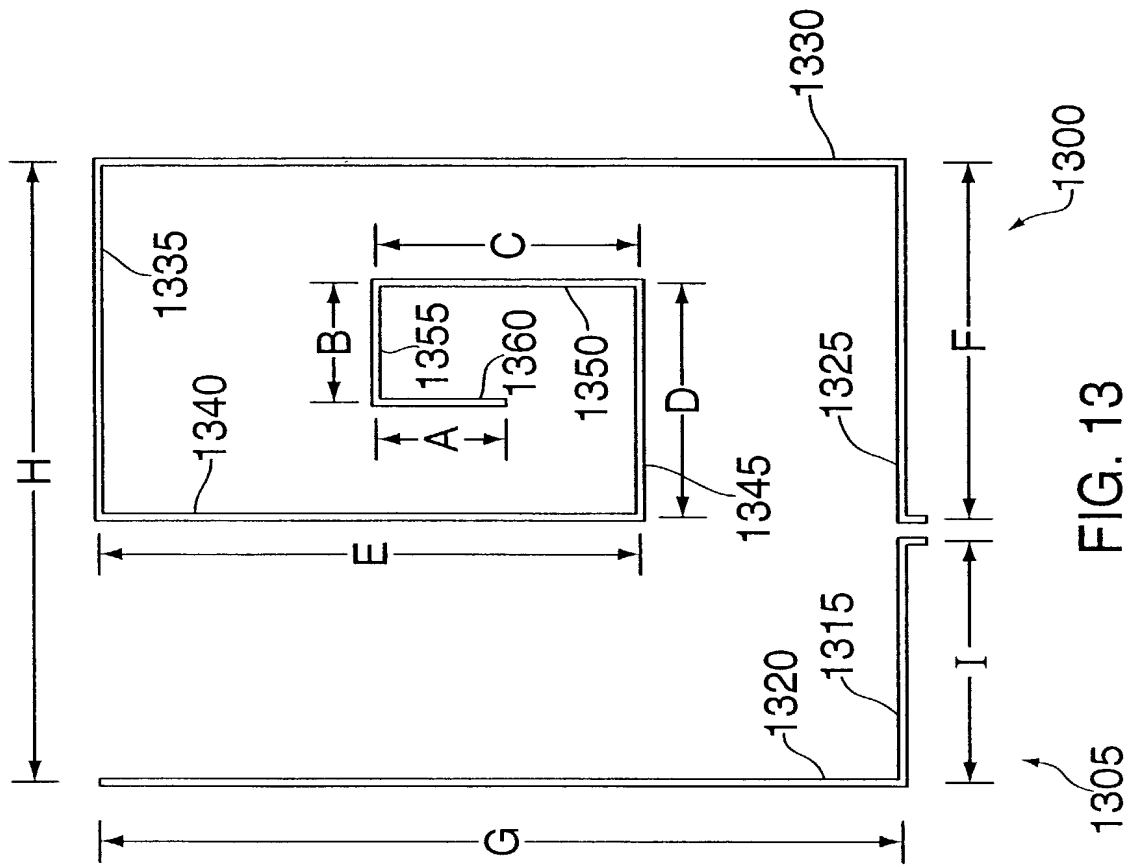
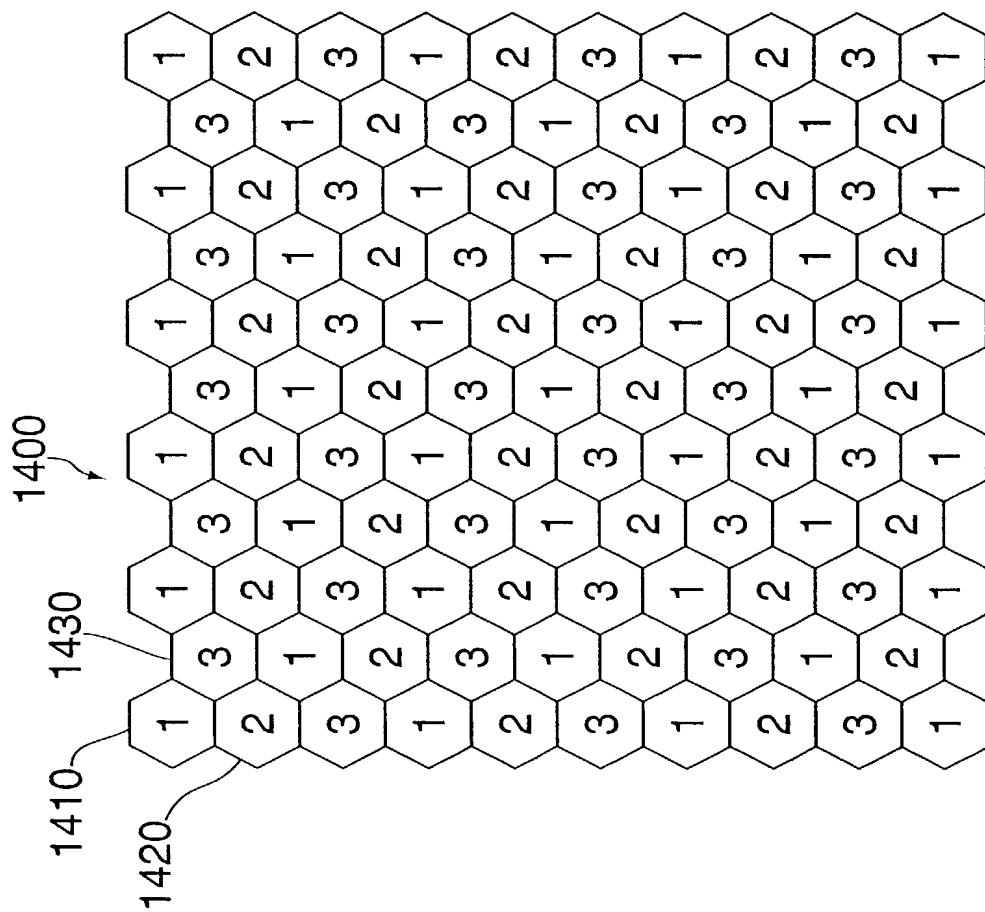


FIG. 11



FIG. 13 is a schematic diagram of a device 1300 in a perspective view. The device 1300 includes a housing 1305, a display 1310, a camera 1315, a speaker 1320, a microphone 1325, a sensor 1330, a processor 1335, a memory 1340, a battery 1345, and a communication module 1350. The device 1300 is configured to perform various functions, including displaying information, capturing images, playing audio, recording audio, sensing environmental conditions, processing data, storing data, providing power, and communicating with other devices.







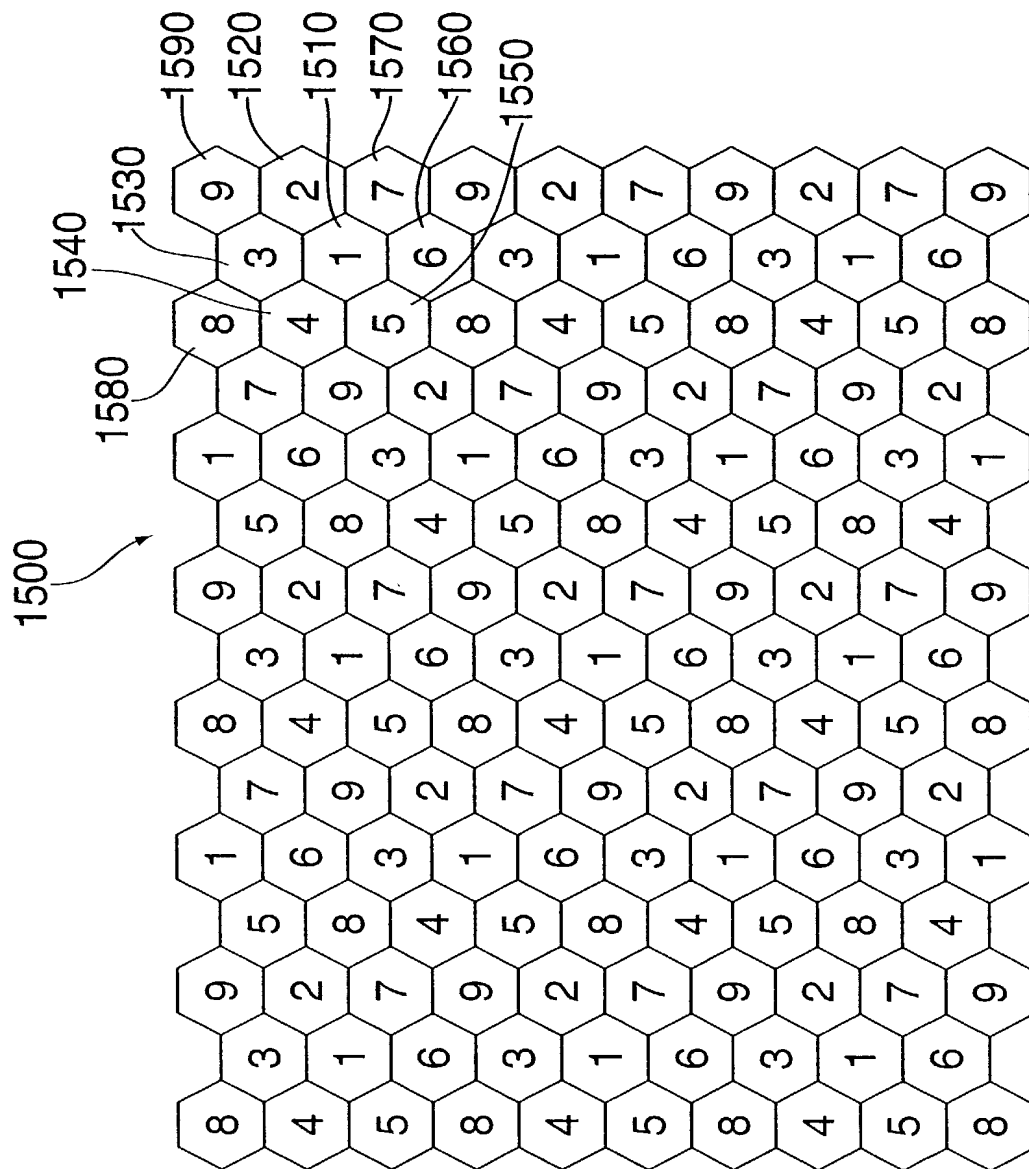
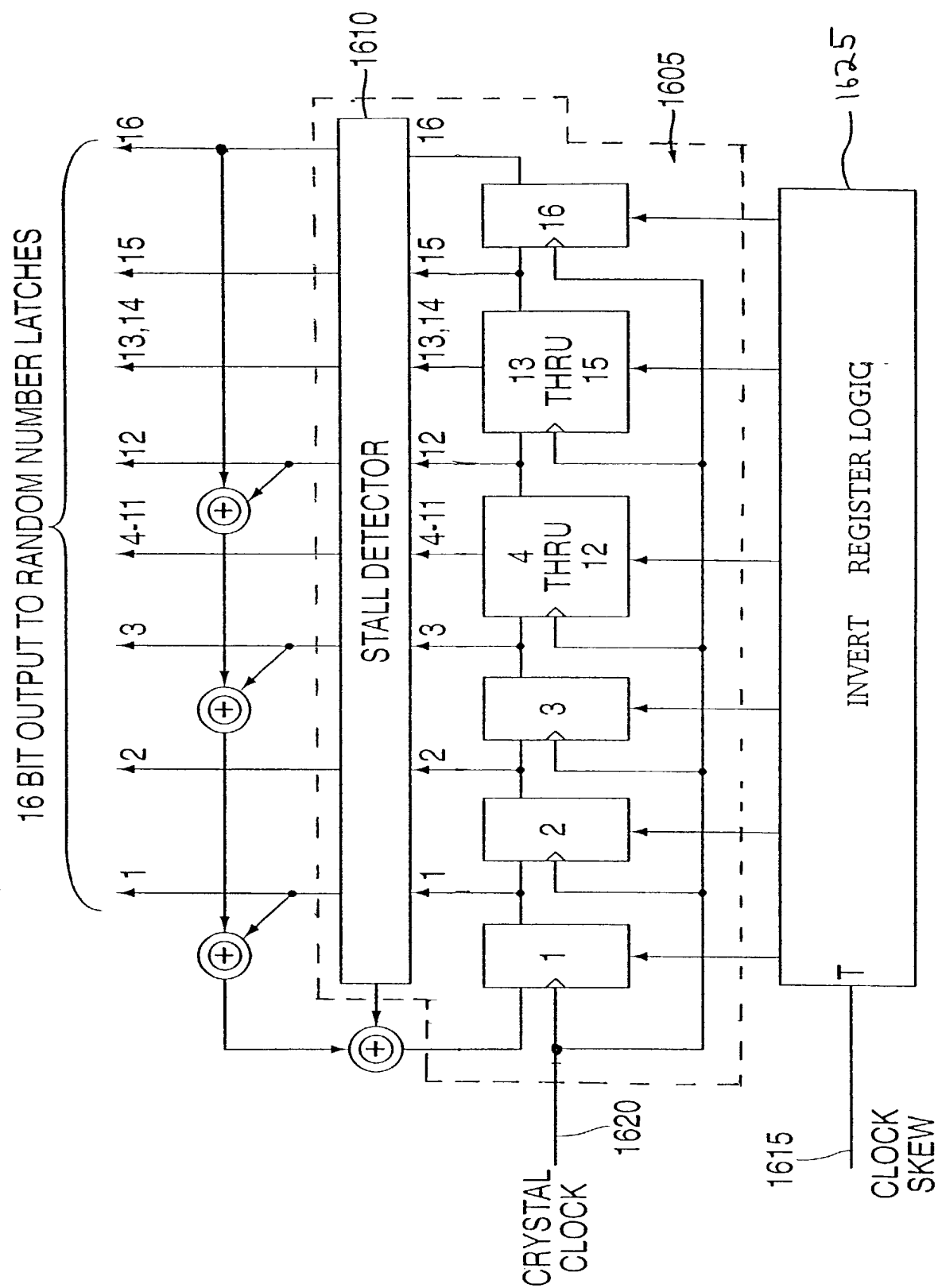


FIG. 15



1175 FIG. 16